

**ABSTRACT**

An array substrate for a liquid crystal display device includes a substrate having a non-display region and a display region gate and data lines crossing each other, a gate electrode connected to the gate line, a gate insulating layer on the gate line and the gate electrode, an active layer on the gate insulating layer over the gate electrode, an ohmic contact layer on the active layer, source and drain electrodes spaced apart from each other on the ohmic contact layer, a pixel electrode connected to the drain electrode and contacting the gate insulating layer, an alignment layer on the pixel electrode, gate and data pads defined as an end portion of the gate and data lines, respectively, and positioned at the non-display region, and gate and data pad terminals on the gate and data pads, respectively. The data pad terminal may extend to the display region.